

**200V N-CHANNEL TRENCH POWER MOSFET**

**MAIN CHARACTERISTICS**

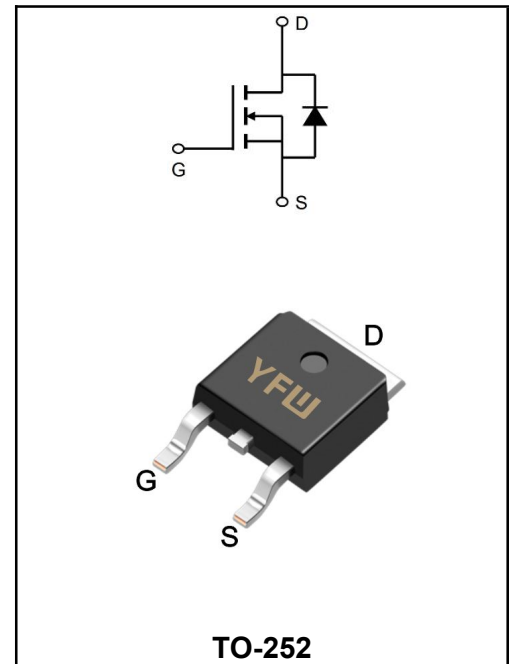
<b>I<sub>D</sub></b>	24A
<b>V<sub>DS</sub></b>	200V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=10V)</sub></b>	< 80mΩ (Typ: 64mΩ)

**FEATURES**

- ◆ High density cell design for ultra low R<sub>ds(on)</sub>
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high E<sub>AS</sub>
- ◆ Excellent package for good heat dissipation
- ◆ Special process technology for high ESD capability
- ◆ **YFW-SGT technology**

**APPLICATIONS**

- ◆ Power switching application
- ◆ Hard switched and high frequency circuits
- ◆ Uninterruptible power supply



**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

Characteristics	Symbol	Value	Unit
Drain-Source Voltage	<b>V<sub>DS</sub></b>	200	<b>V</b>
Gate-Source Voltage	<b>V<sub>GS</sub></b>	±20	<b>V</b>
Continue Drain Current	<b>I<sub>D</sub></b>	24	<b>A</b>
Drain Current-Continuous(T <sub>c</sub> =100°C)	<b>I<sub>D</sub></b>	17	<b>A</b>
Pulsed Drain Current	<b>I<sub>DM</sub></b>	100	<b>A</b>
Power Dissipation	<b>P<sub>D</sub></b>	150	<b>W</b>
Single Pulse Avalanche Energy <sup>(Note5)</sup>	<b>E<sub>AS</sub></b>	250	<b>mJ</b>
Operating Temperature Range	<b>T<sub>J</sub></b>	175	<b>°C</b>
Storage Temperature Range	<b>T<sub>STG</sub></b>	-55 to +175	<b>°C</b>
Thermal Resistance, Junction to Case <sup>(Note 2)</sup>	<b>R<sub>θJC</sub></b>	1	<b>°C/W</b>

**Maximum Ratings at Tc=25°C unless otherwise specified**

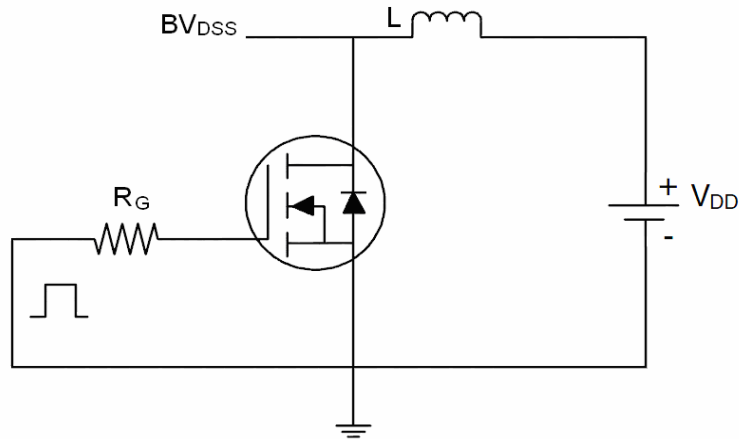
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	<b>BV<sub>DSS</sub></b>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	200	220	-	<b>V</b>
Zero Gate Voltage Drain Current	<b>I<sub>DSS</sub></b>	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V	-	-	1	<b>μA</b>
Gate-Body Leakage Current	<b>I<sub>GSS</sub></b>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	<b>nA</b>
Gate Threshold Voltage	<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.5	3.2	4	<b>V</b>
Drain-Source On-State Resistance	<b>R<sub>DS(ON)</sub></b>	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	-	64	80	<b>mΩ</b>
Forward Transconductance	<b>g<sub>FS</sub></b>	V <sub>DS</sub> =50V, I <sub>D</sub> =15A	30	-	-	<b>S</b>
Input Capacitance	<b>C<sub>iss</sub></b>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1.0MHz		4180		<b>pF</b>
Output Capacitance	<b>C<sub>oss</sub></b>			162		
Reverse Transfer Capacitance	<b>C<sub>rss</sub></b>			73		
Turn-on Delay Time	<b>t<sub>d(on)</sub></b>	V <sub>DD</sub> =100V, I <sub>D</sub> =15A V <sub>GS</sub> =10V, R <sub>GEN</sub> =2.5Ω	-	10	-	<b>nS</b>
Turn-on Rise Time	<b>t<sub>r</sub></b>		-	18	-	
Turn-Off Delay Time	<b>t<sub>d(off)</sub></b>		-	22	-	
Turn-Off Fall Time	<b>t<sub>f</sub></b>		-	5	-	
Total Gate Charge	<b>Q<sub>g</sub></b>	V <sub>DS</sub> =100V, I <sub>D</sub> =15A, V <sub>GS</sub> =10V		60		<b>nC</b>
Gate-Source Charge	<b>Q<sub>gs</sub></b>			19		
Gate-Drain Charge	<b>Q<sub>gd</sub></b>			17		
Diode Forward Voltage (Note 3)	<b>V<sub>SD</sub></b>	V <sub>GS</sub> =0V, I <sub>S</sub> =11A	-	-	1.2	<b>V</b>
Diode Forward Current (Note 2)	<b>I<sub>S</sub></b>	-	-	-	24	<b>A</b>
Reverse Recovery Time	<b>t<sub>rr</sub></b>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 15A di/dt = 100A/μs <sup>(Note3)</sup>	-	90	-	<b>nS</b>
Reverse Recovery Charge	<b>Q<sub>rr</sub></b>		-	300	-	<b>nC</b>
Forward Turn-On Time	<b>t<sub>on</sub></b>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

**Notes:**

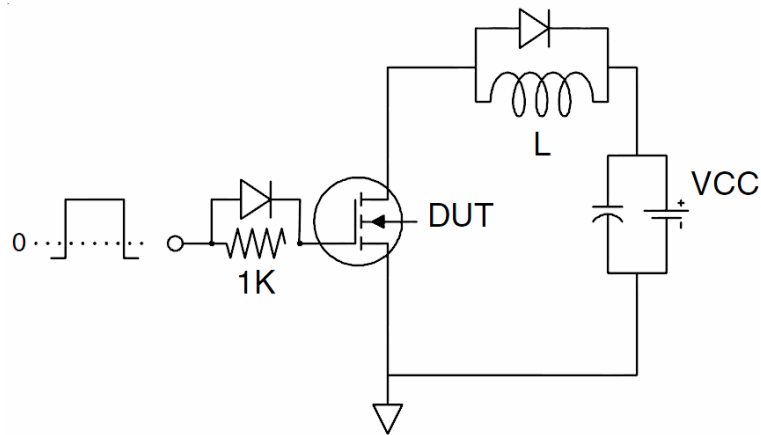
- 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- 4.Guaranteed by design, not subject to production
- 5.EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=100V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω

**Test Circuit**

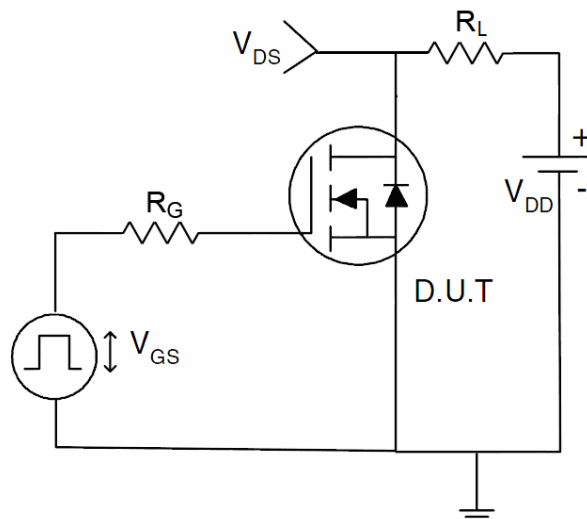
**1) E<sub>AS</sub> test Circuits**



**2) Gate charge test Circuit**



**3) Switch Time Test Circuit**



Typical Electrical and Thermal Characteristics (Curves)

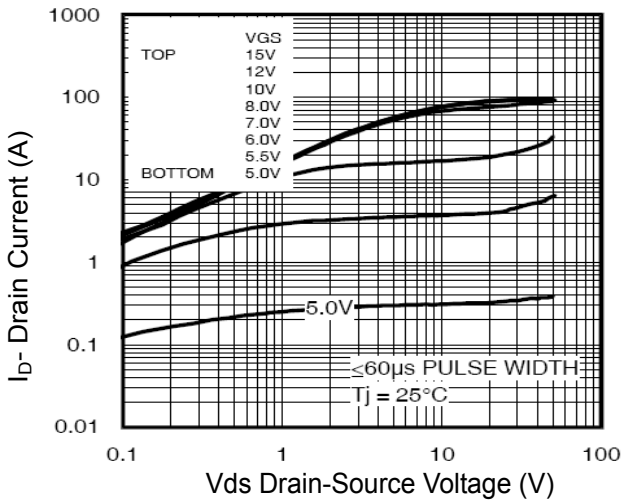


Figure 1 Output Characteristics

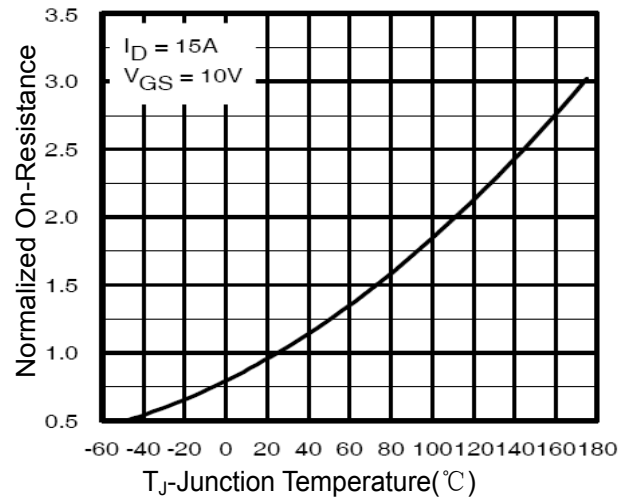


Figure 4  $R_{dson}$ -Junction Temperature

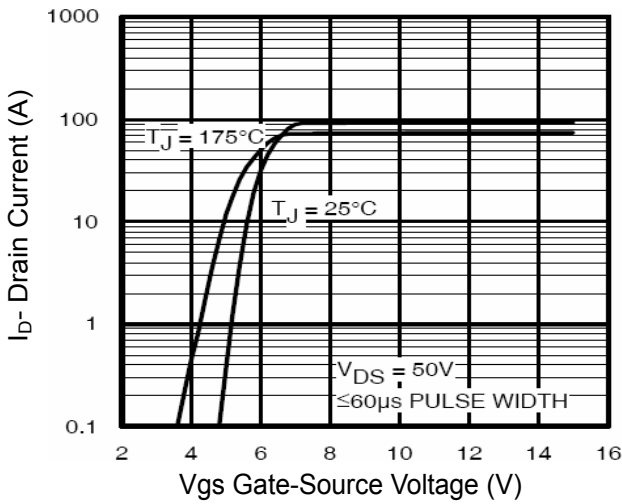


Figure 2 Transfer Characteristics

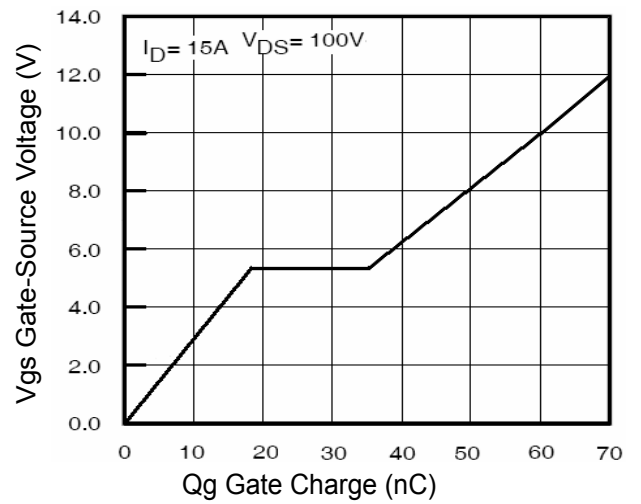


Figure 5 Gate Charge

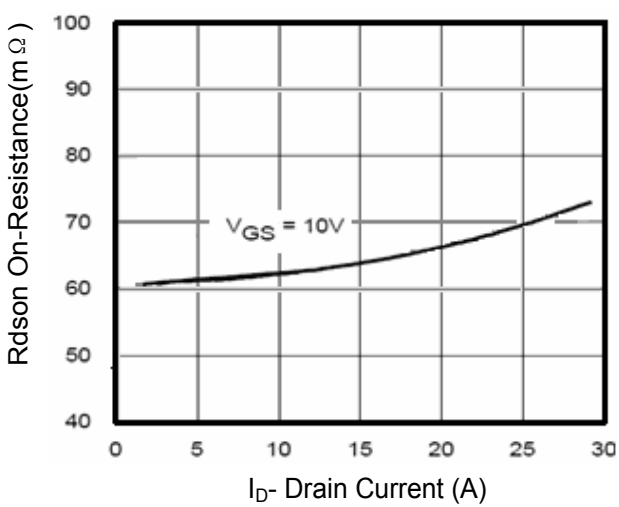


Figure 3  $R_{dson}$ - Drain Current

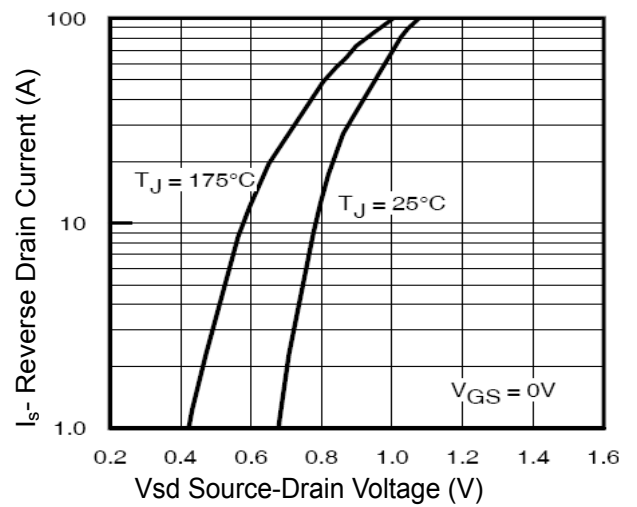
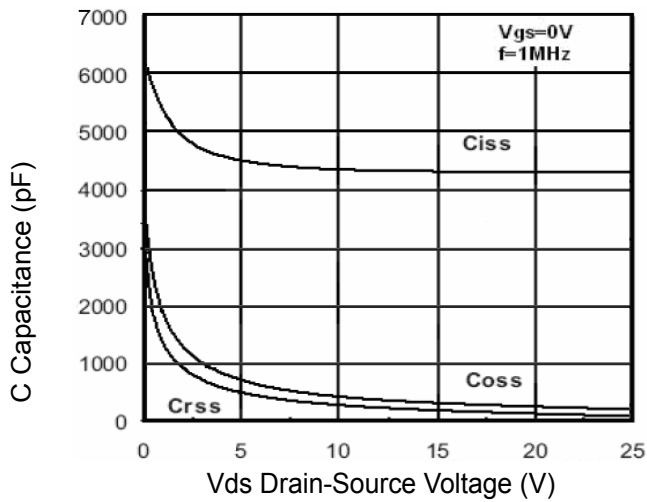
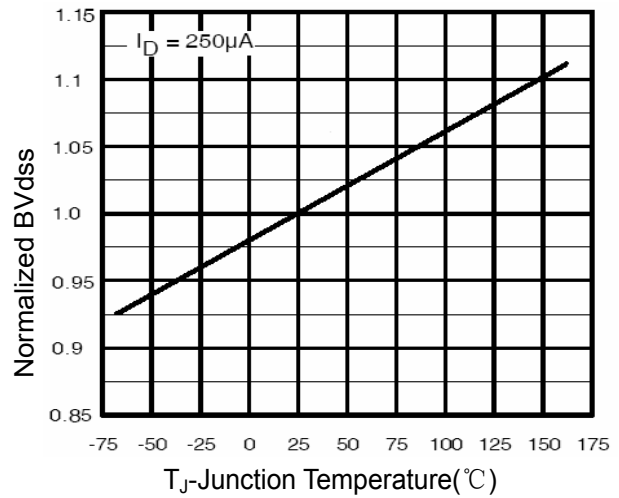


Figure 6 Source- Drain Diode Forward

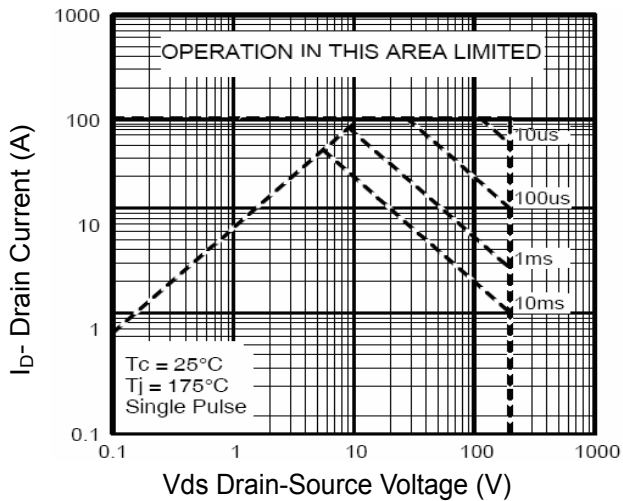
Typical Electrical and Thermal Characteristics (Curves)



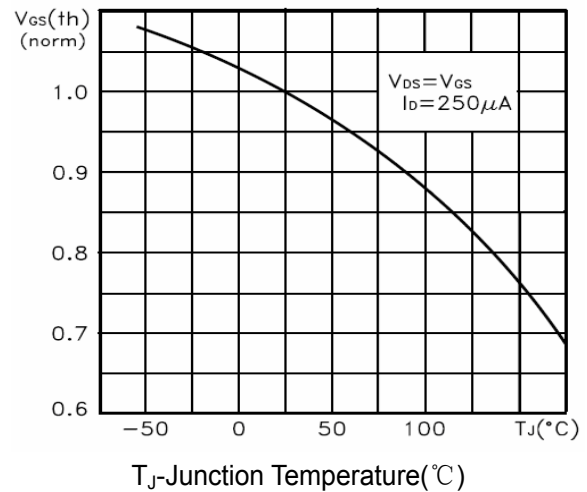
**Figure 7 Capacitance vs Vds**



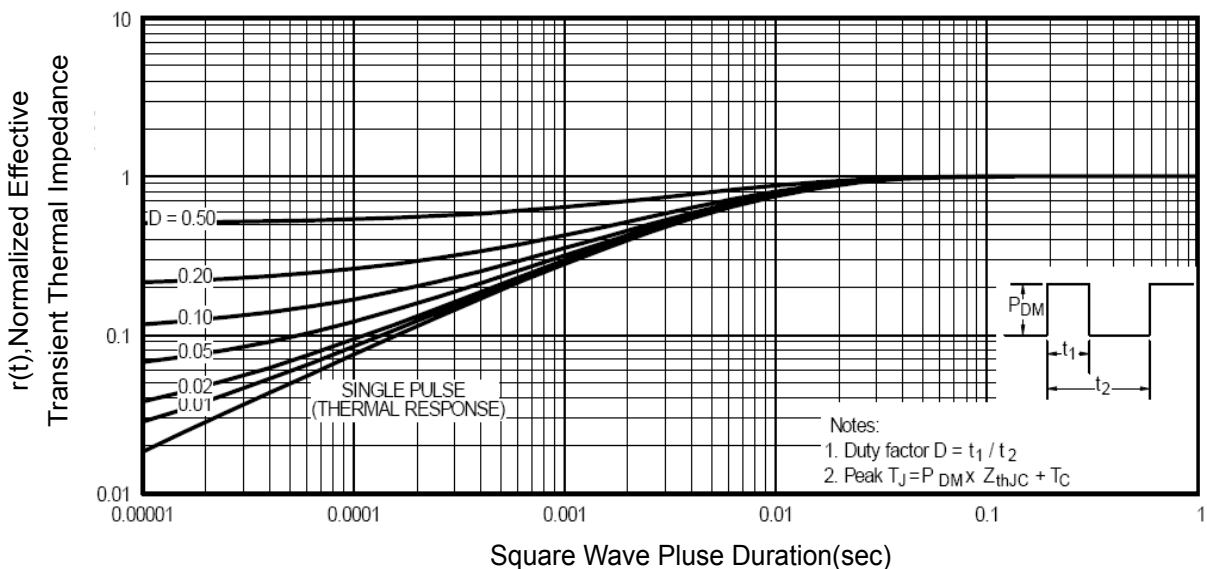
**Figure 9 BV<sub>DSS</sub> vs Junction Temperature**



**Figure 8 Safe Operation Area**

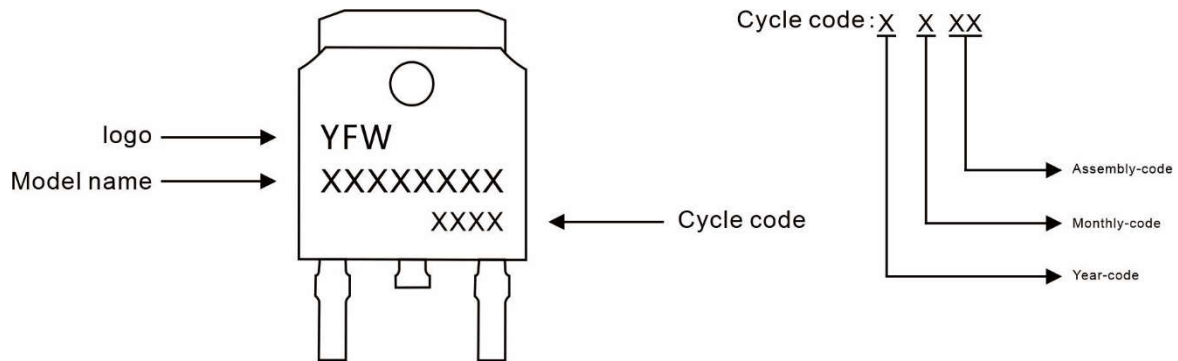


**Figure 10 V<sub>GS(th)</sub> vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

**Marking Diagram**



**Ordering information**

Model name	Package	Unit Weight	Base Quantity	Packing Quantity
YFWG24N20AD	TO-252	0.011oz(0.32g)	2500pcs/reel	5000pcs/box 25000pcs/Carton

**Package Dimensions**

**TO-252**

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.50	0.087	0.098
A1	0.00	0.12	0.000	0.005
A2	2.20	2.40	0.087	0.094
B	1.20	1.60	0.047	0.063
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.35	6.65	0.250	0.262
D1	5.20	5.40	0.205	0.213
E	5.40	5.70	0.213	0.224
e	2.20	2.40	0.087	0.094
e1	4.40	4.80	0.173	0.189
L	10.00	11.00	0.393	0.433
L1	2.70	3.10	0.106	0.122
L2	1.40	1.80	0.055	0.071
L3	0.90	1.50	0.035	0.059

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